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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065626	11/04/2002	365	230.06	2818	HOANG, HUAN

\*\*APPLICANTS: Atallah Francois; Burda Gregory; Corrae Anthony;

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	RPS920020085
Verified and Acknowledged Examiners's initials		
TITLE : Method and circuit for optimizing power consumption and performance of driver circuits		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due		Sheets Drawn	Print Fig.
Date Paid		Figs. Drawn	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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